



Kabellänge: 500mm

FSC D2703-S; Hirose DF13-40, straight, SMT			AUO-G150XG01; DF-14H-20P-1.25H (Hirose) or CWY20G-A0D1T (PTWO)		
LVDS-Connector			Pin No. Symbol		
Ground	GND	1			
Ground	GND	2			
LVDS_Out3+ (ODD_3+)	LO3+	3	20	X	GND
LVDS_Out7+ (EVEN_3+)	LO7+	4	18	X	RxIN3+
LVDS_Out3- (ODD_3-)	LO3-	5	17	X	RxIN3-
LVDS_Out7- (EVEN_3-)	LO7-	6			
Ground	GND	7	3		GND
Ground	GND	8	4		GND
LVDS_Out2+ (ODD_2+)	LO2+	9	12		RxIN2+
LVDS_Out6+ (EVEN_2+)	LO6+	10			
LVDS_Out2- (ODD_2-)	LO2-	11	11		RxIN2-
LVDS_Out6- (EVEN_2-)	LO6-	12			
Ground	GND	13			
Ground	GND	14	7		GND
LVDS_Out1+ (ODD_1+)	LO1+	15	9		RxIN1+
LVDS_Out5+ (EVEN_1+)	LO5+	16			
LVDS_Out1- (ODD_1-)	LO1-	17	8		RxIN1-
LVDS_Out5- (EVEN_1-)	LO5-	18			
Ground	GND	19			
Ground	GND	20	10	↔	GND
LVDS_Out0+ (ODD_0+)	LO0+	21	6		RxIN0+
LVDS_Out4+ (EVEN_0+)	LO4+	22			
LVDS_Out0- (ODD_0-)	LO0-	23	5		RxIN0-
LVDS_Out4- (EVEN_0-)	LO4-	24			
Ground	GND	25			
Ground	GND	26	13		GND
LVDS_CLK1+ (CLK_ODD+)	CLK1+	27	15		CKIN+
LVDS_CLK2+ (CLK_EVEN+)	CLK2+	28			
LVDS_CLK1- (CLK_ODD-)	CLK1-	29	14		CKIN-
LVDS_CLK2- (CLK_EVEN-)	CLK2-	30			
Ground	GND	31	16		GND
Ground	GND	32	19		GND
DDC-Clock	DDCCLK	33			
DDC-Data	DDCDATA	34			
LCD-Power <sup>1)</sup>	+3.3V / +5V	35	1		VDD
LCD-Power <sup>1)</sup>	+3.3V / +5V	36	2		VDD
LCD-Power <sup>1)</sup>	+3.3V / +5V	37			
Ground	GND	38			
Ground	GND	39			
LCD_PowerOn	LCD_On	40			

1) selectable via Jumper

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FSC D2703-S; JST PHR-8			Green C&C GH001A		
Inverter-Connector			12505WR-10A00(P) (YEONHO) / EQUIVALENT		
Ground	GND	1	3		GND
Ground	GND	2	4		GND
Backlight Brightness CTRL	tbd	3	1		BRT_Adj
Power 5V	VCC	4			
Power 5V	VCC	5			
Backlight On/Off Control	BL On/Off	6	5		BL On/Off
Power 12V	+12V	7	9		DC-In / 12V
Power 12V	+12V	8	10		DC-In / 12V

LVDS TFT / Inverter Steckerbelegung  
Ausgabe 1.1 06.07.2007

**LVDS-Belegung lt. Datenblatt**

Pin No.	Symbol	Description
		DF-14H-20P-1.25H (Hirose) or CWY20G-A0D1T (PTWO)
1	VDD	Power Supply, 3.3V (typical)
2	VDD	Power Supply, 3.3V (typical)
3	VSS	Ground
4	VSS	Ground
5	Rin0-	- LVDS differential data input (R0-R5, G0)
6	Rin0+	+ LVDS differential data input (R0-R5, G0)
7	VSS	Ground
8	Rin1-	- LVDS differential data input (G1-G5, B0-B1)
9	Rin1+	+ LVDS differential data input (G1-G5, B0-B1)
10	VSS	Ground
11	Rin2-	- LVDS differential data input (B2-B5, HS, VS, DE)
12	Rin2+	+ LVDS differential data input (B2-B5, HS, VS, DE)
13	VSS	Ground
14	ClkIN-	- LVDS differential clock input
15	ClkIN+	+ LVDS differential clock input
16	VSS	Ground
17	Rin3-	NC
18	Rin3+	NC
19	VSS	Ground
20	NC	Please "floating" and don't connect to ground.

**LVDS-Erweiterung für Philips LM150X08  
LCD (CN1):DF14H-20P-1.25H (Hirose)**

Pin No	Symbol
1	VLCD
2	VLCD
3	GND
4	GND
5	RXIN0-
6	RXIN0+
7	GND
8	RXIN1-
9	RXIN1+
10	GND
11	RXIN2-
12	RXIN2+
13	GND
14	RXCLK IN-
15	RXCLK IN+
16	GND
X	RXIN3-
X	RXIN3+
19	GND
X	GND

Symbol	Parameter	Min	Typ	Max	Unit	Condition
VDD	LCD Drive Voltage	3.0	3.3	3.6	[V]	
IDD	LCD Drive Current	-	1.0	1.3	[A]	VDD=3.3V, All Black Pattern
PDD	LCD Drive power consumption	-	3.3	4.3	[Watt]	VDD=3.3V, All Black Pattern
VDDns	Allowable LCD Drive Ripple Noise	-	-	100	[mV] P-P	

**Inverter-Belegung lt. Datenblatt**

**4.1 CN1 CONNECTOR :12505WR-10A00(P) (YEONHO) / EQUIVALENT**

PIN NO	SYMBOL	REMARK
1	BRT_ADJ	0 ~ 5V
3,4,7,8	GND	GND
5	BL ON/OFF	CCFL Drive SIGNAL(Active HIGH)
2,6	N.C	
9,10	DC-IN(Vin)	DC INPUT Power (11.5~ 12.5V)